002260 E879960

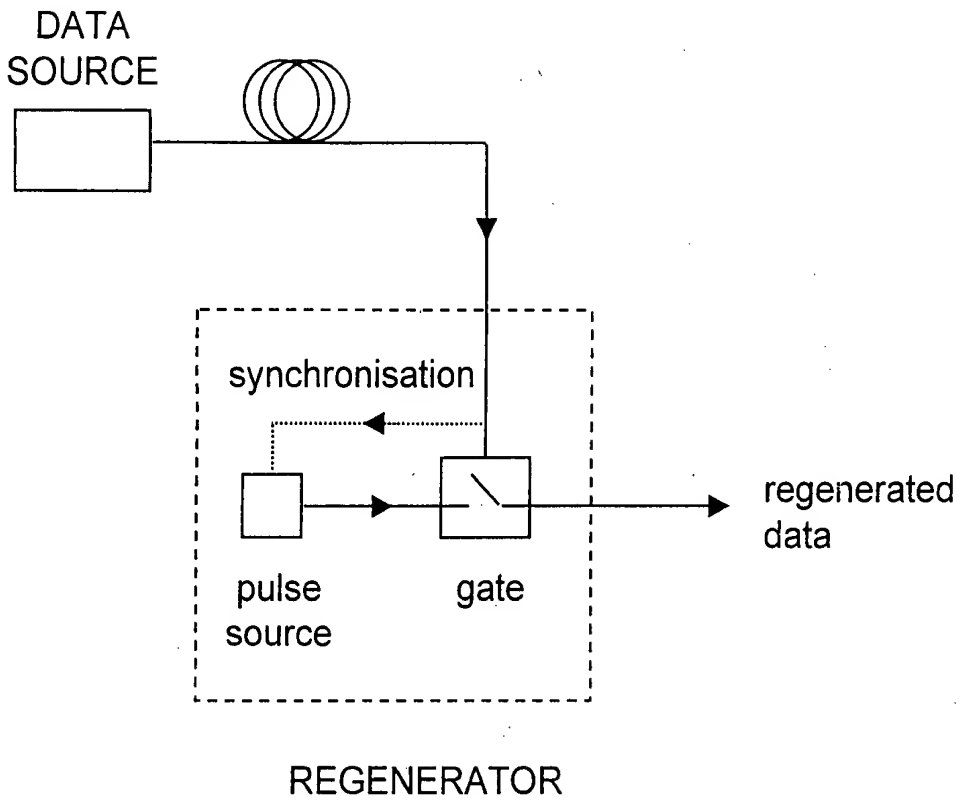

Figure 1

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Figure 2

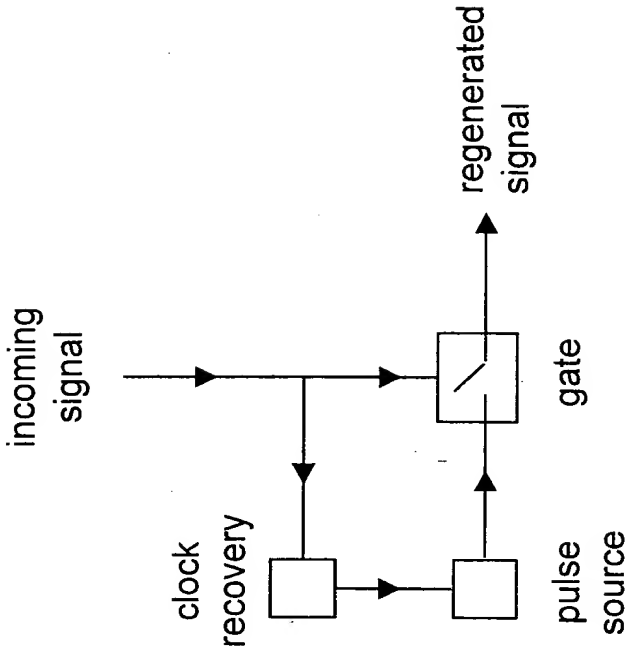
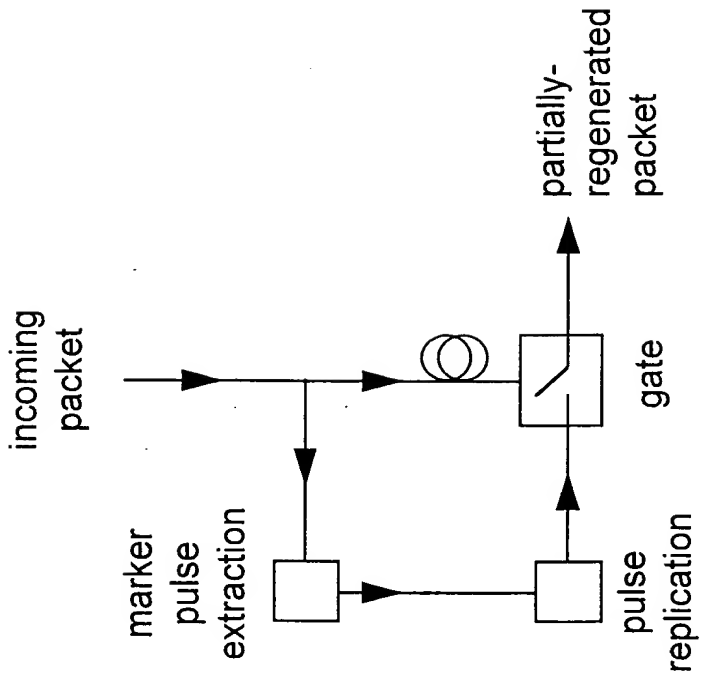

regenerated signal
incoming signal
gate
clock recovery
pulse source

Figure 3


incoming
packet
marker
pulse
extraction
pulse
replication
gate
partially-
regenerated
packet

002260 E879h960

09/694783 00R 332 5 01/04/00 15:25

Figure 4

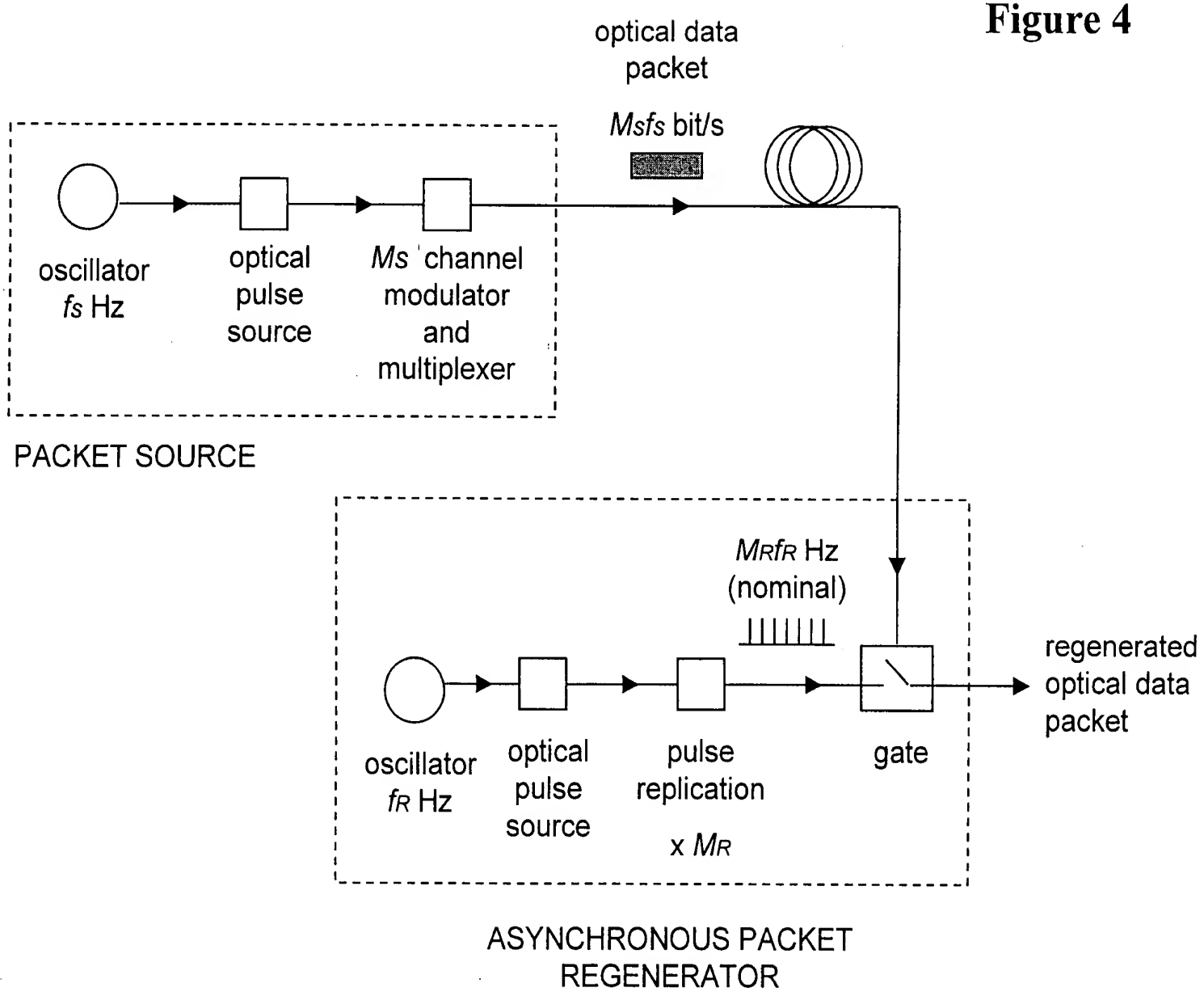


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Figure 5

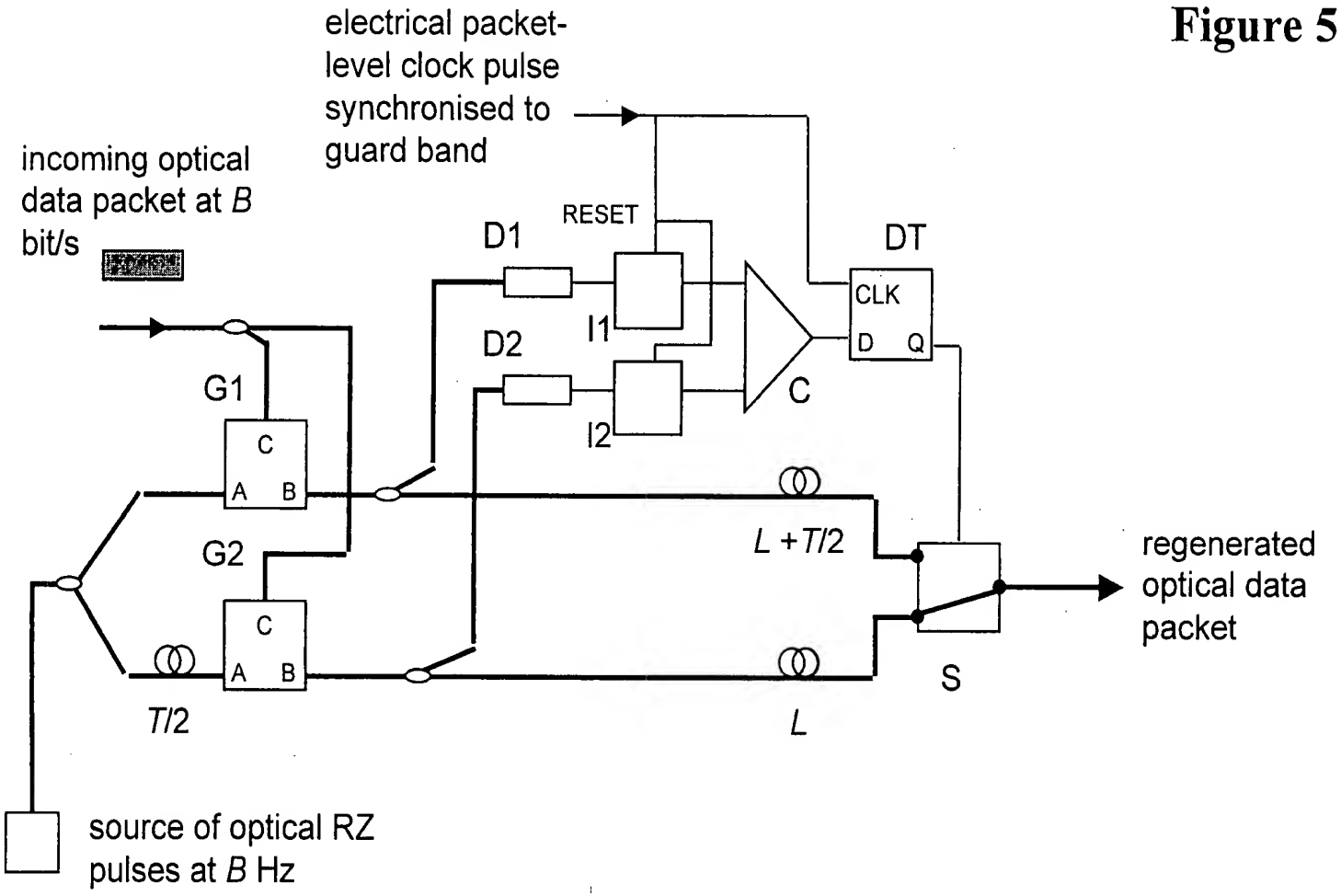

Figure 6

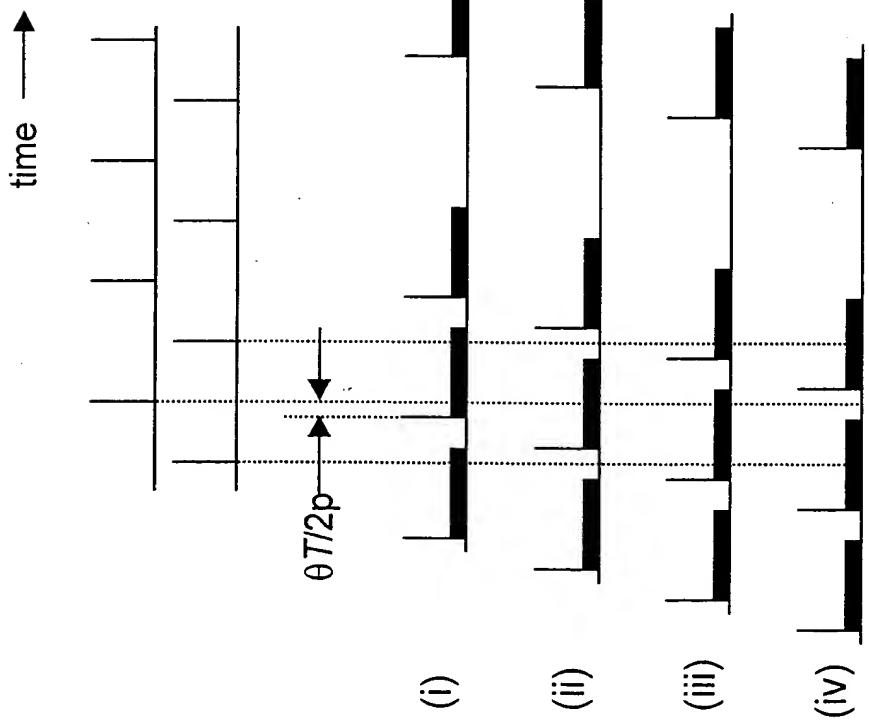
time
clock input to G1
clock input to G2 (delayed by T/2 relative to G1)
θT/2p
incoming packet data bits (11101) and corresponding gate windows, for various phase angles θ
(i)
(ii)
(iii)
(iv)

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Figure 7

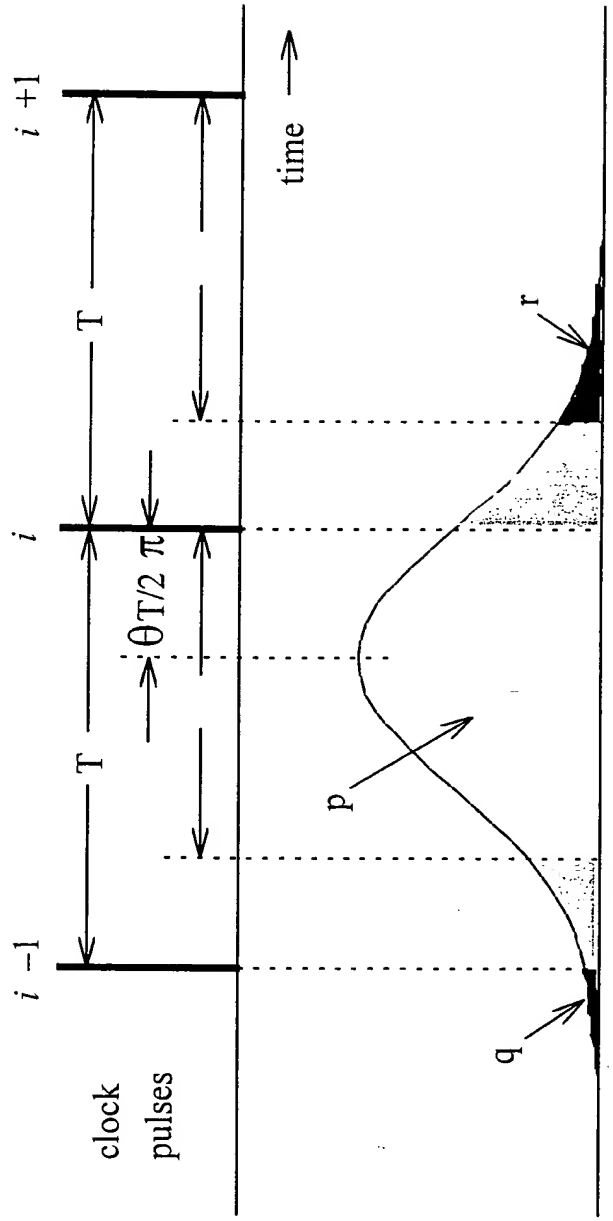

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Figure 8

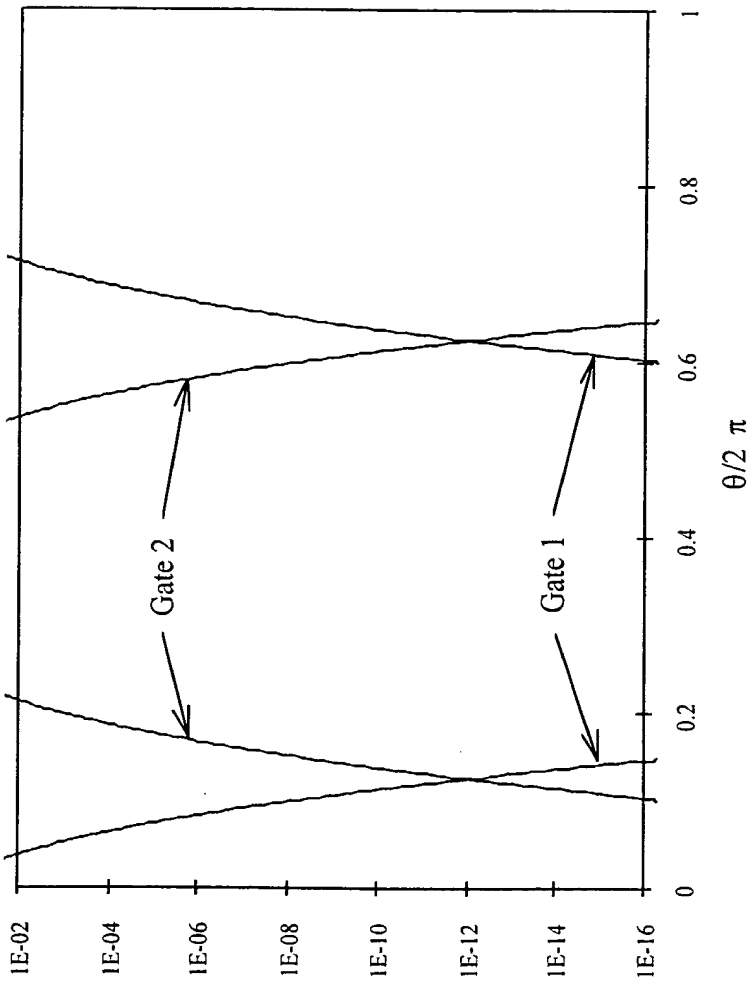

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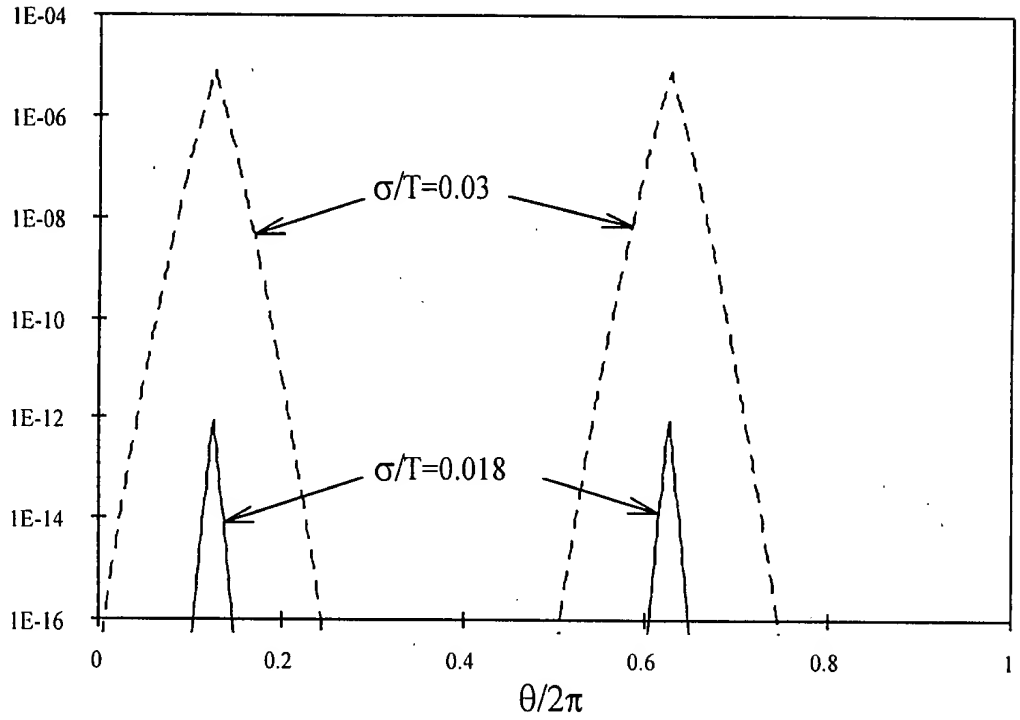
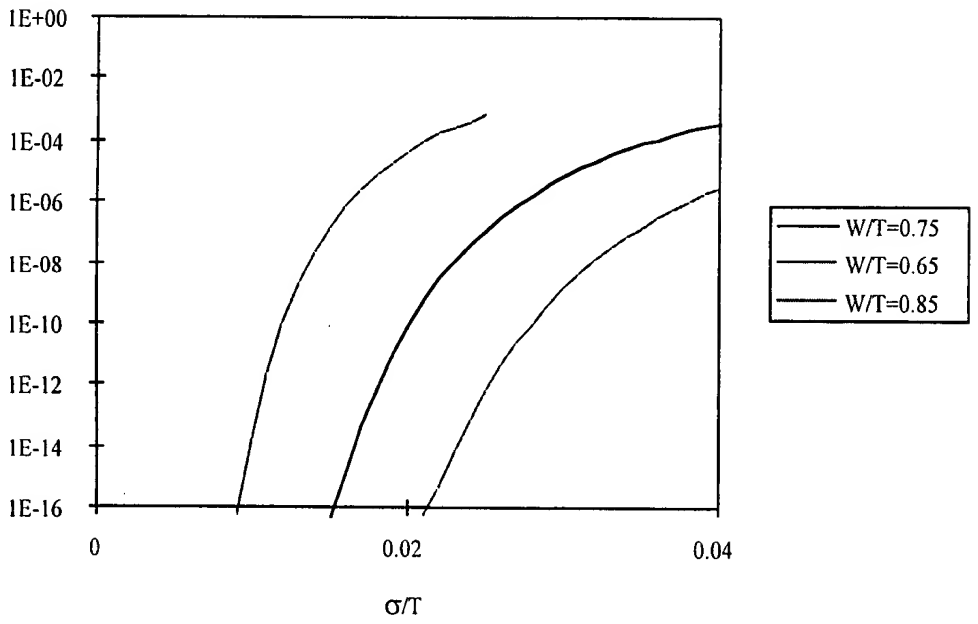
Figure 9



002260 "E829h960

09/646783

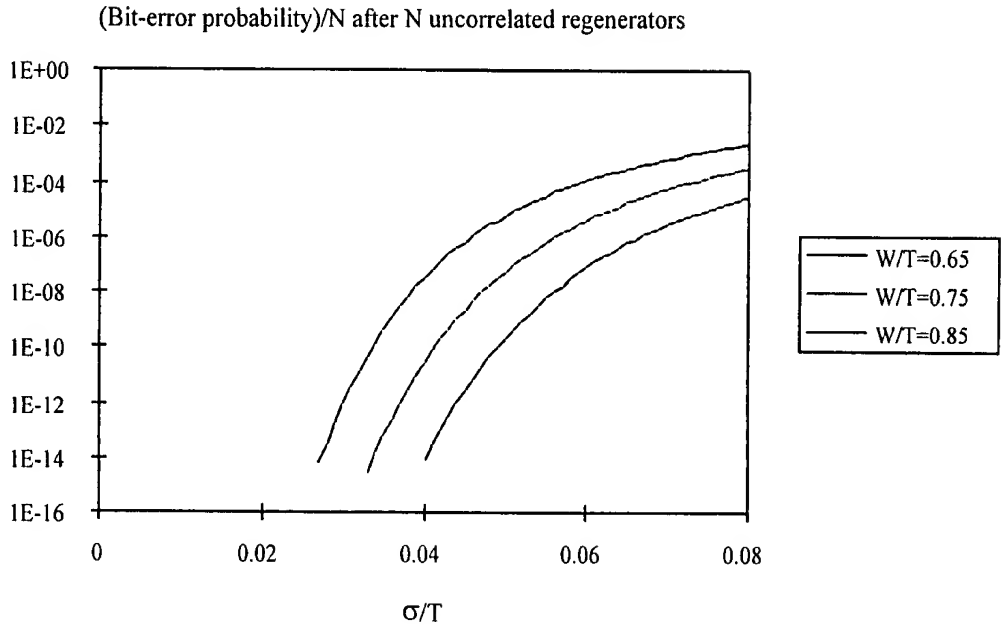
Figure 10



002260 E829H960

09/649783

Figure 11



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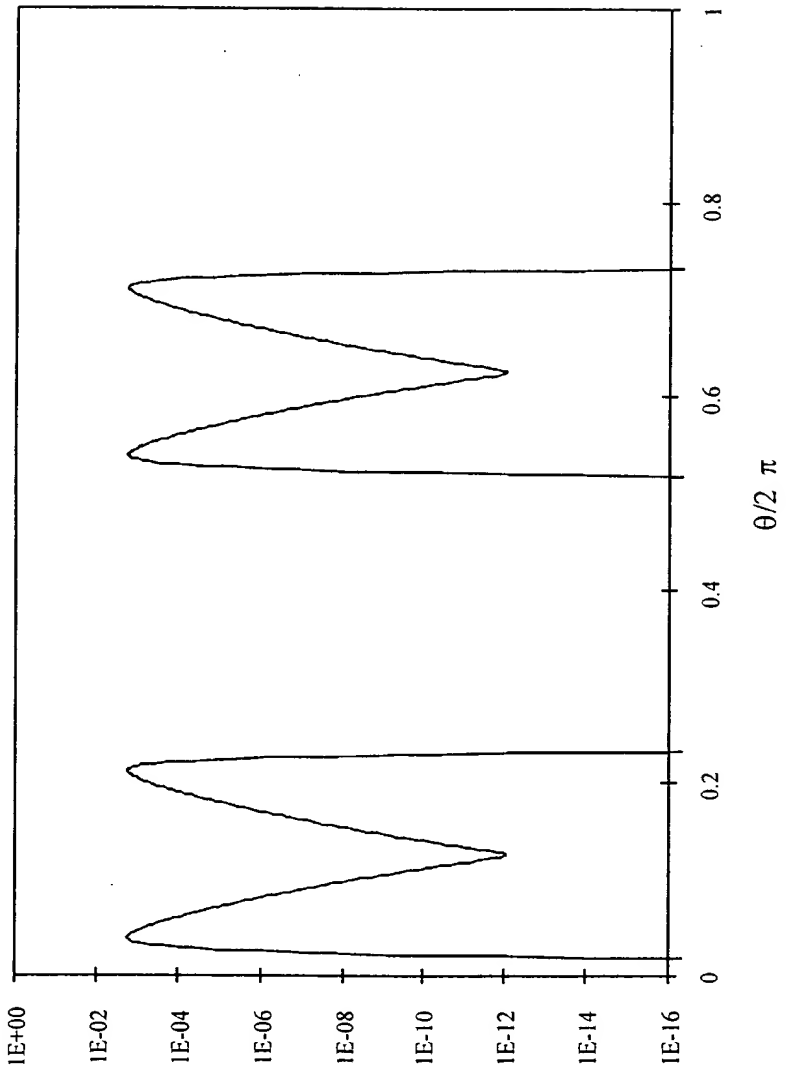
PCT/NL00/00650 " 09/646783

Figure 12



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PCT/US00/24960 09/646783

Figure 13

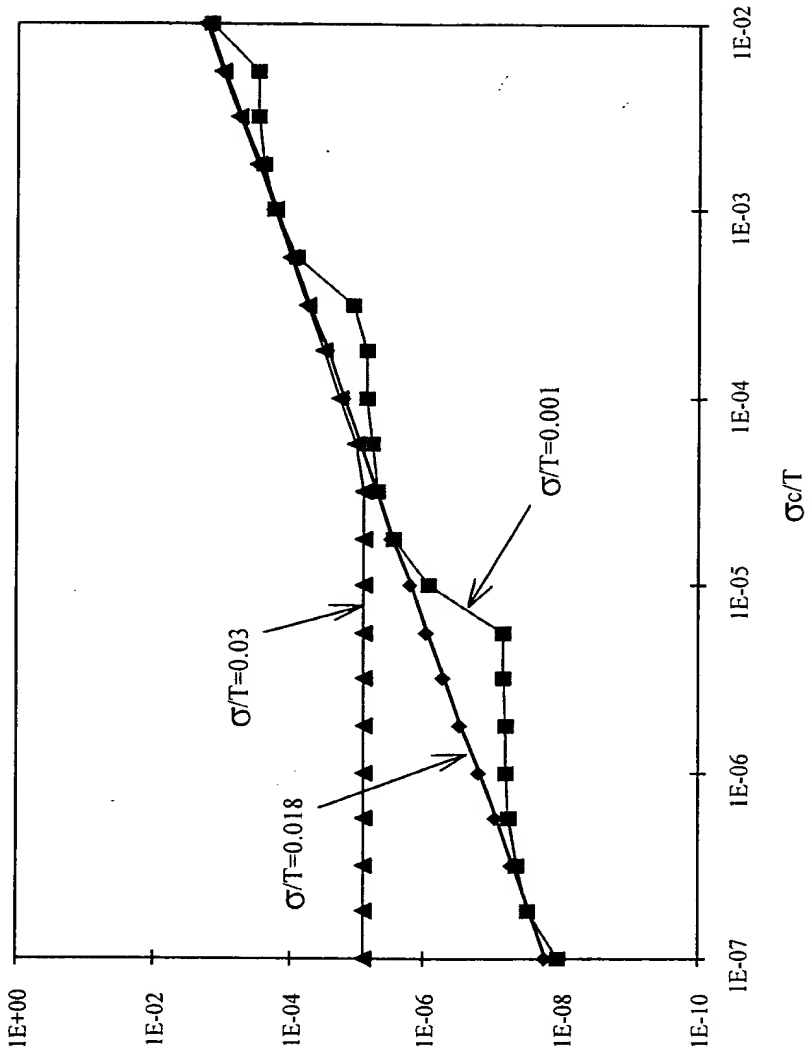

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Figure 14

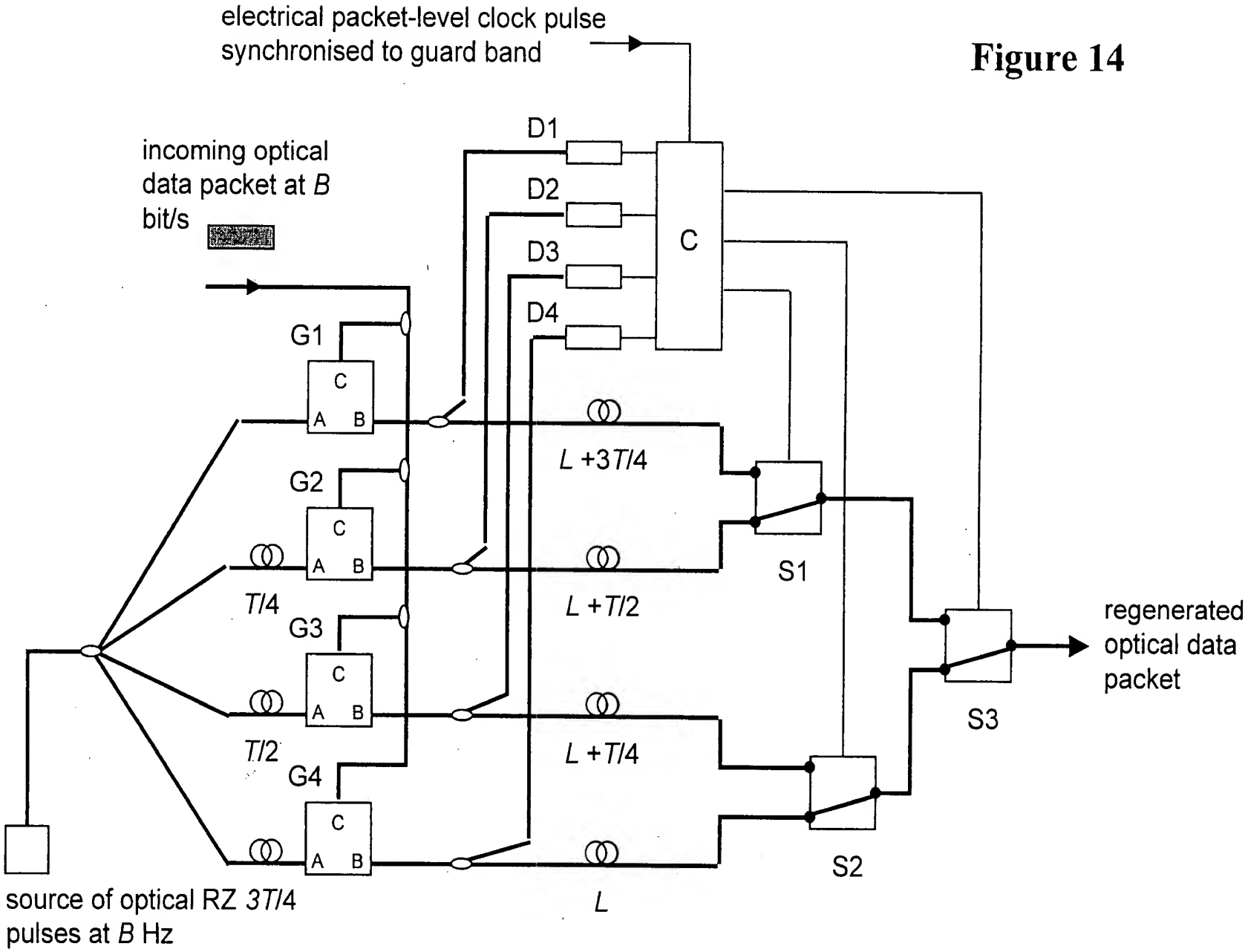

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Figure 15

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Figure 16

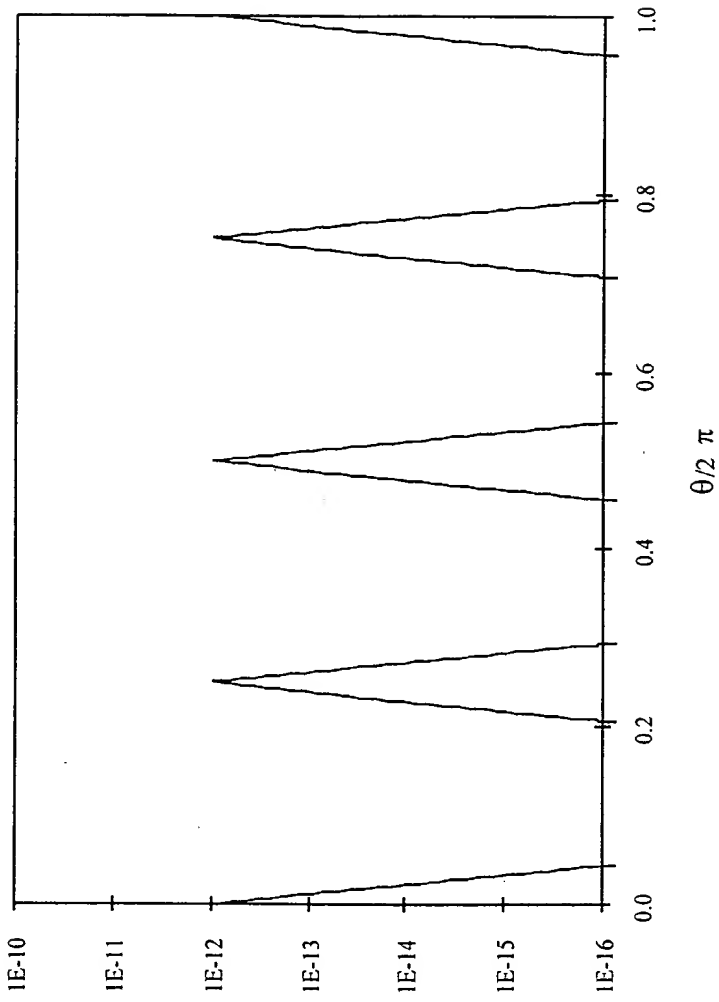

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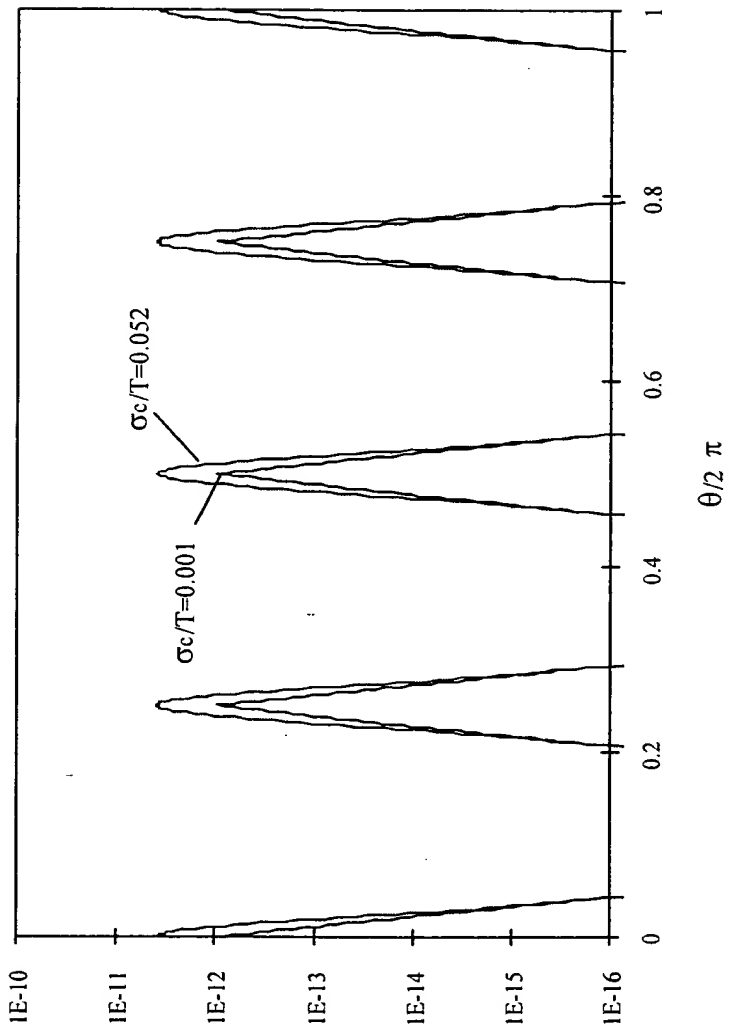
Figure 17



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002260 E829h960

09/646783

Figure 18

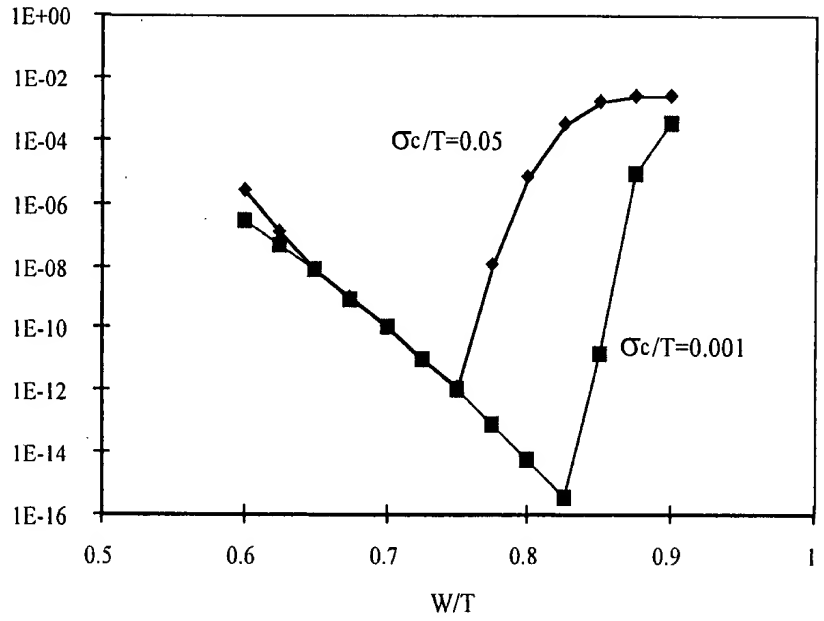

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Figure 19

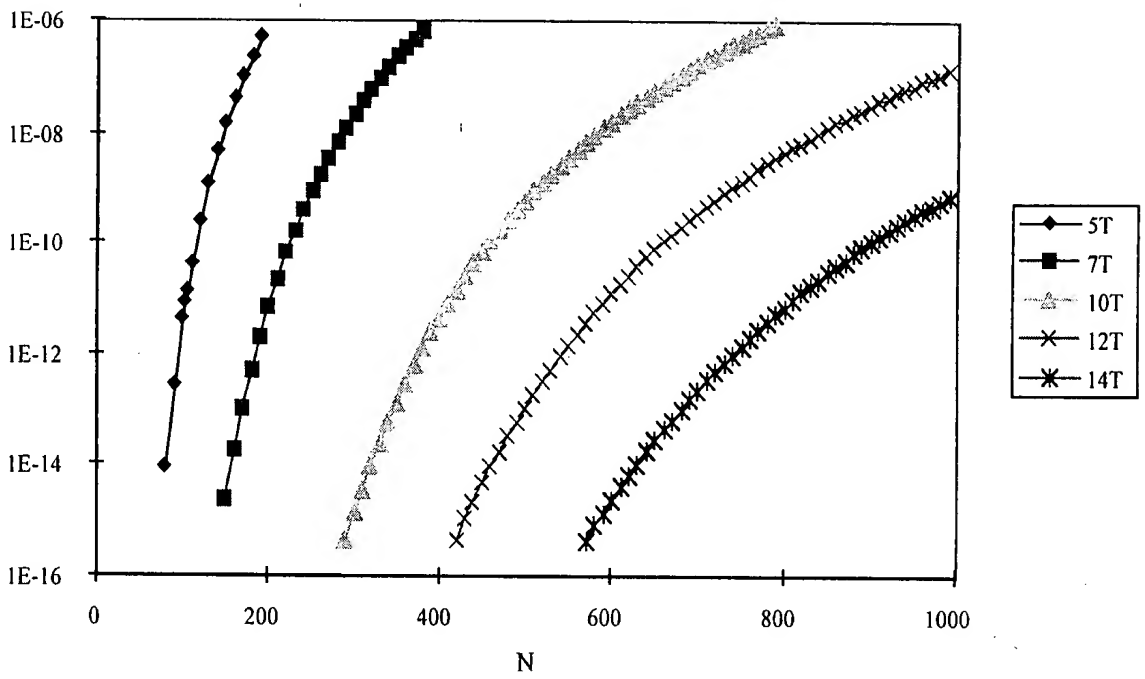

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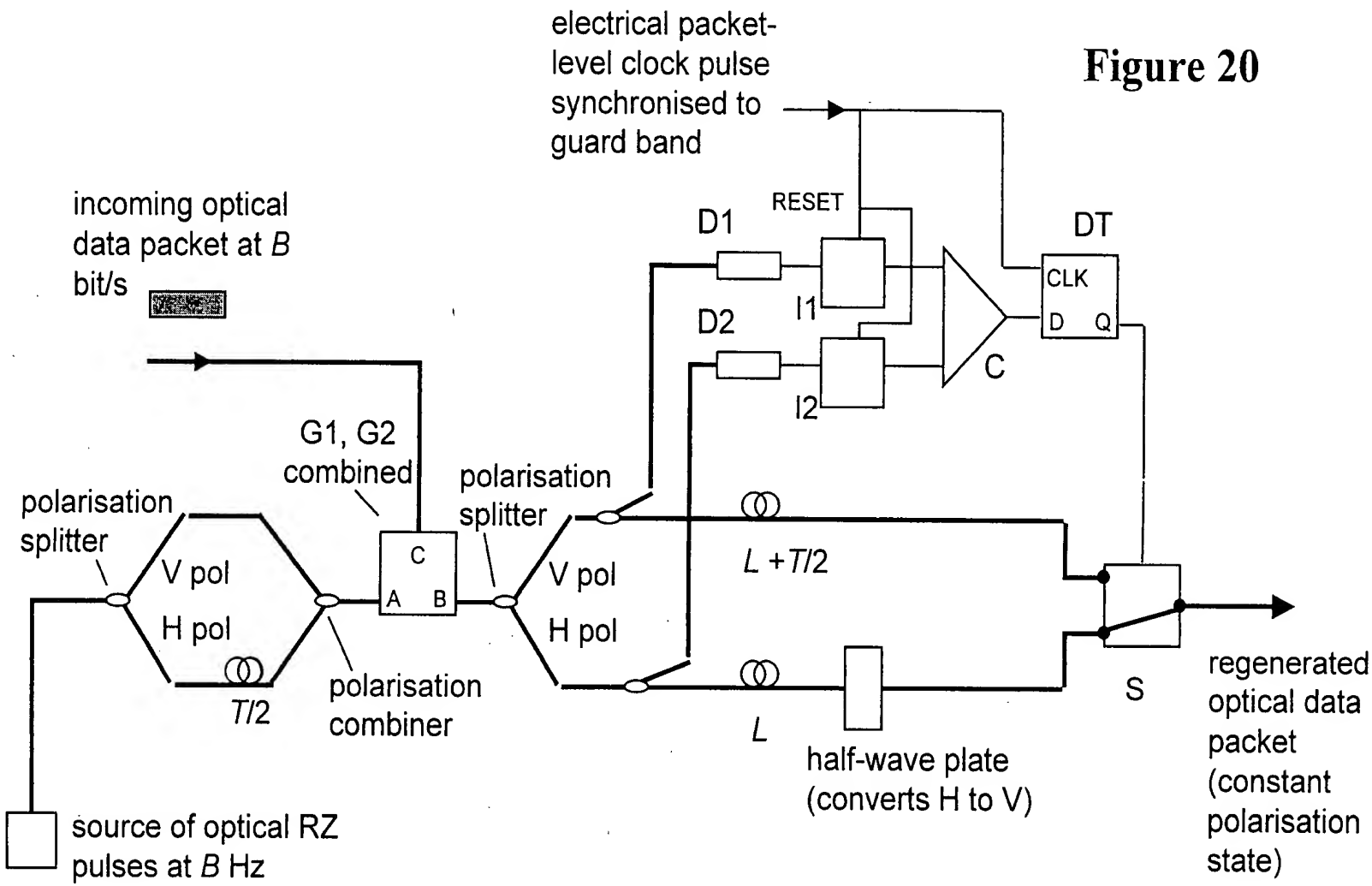
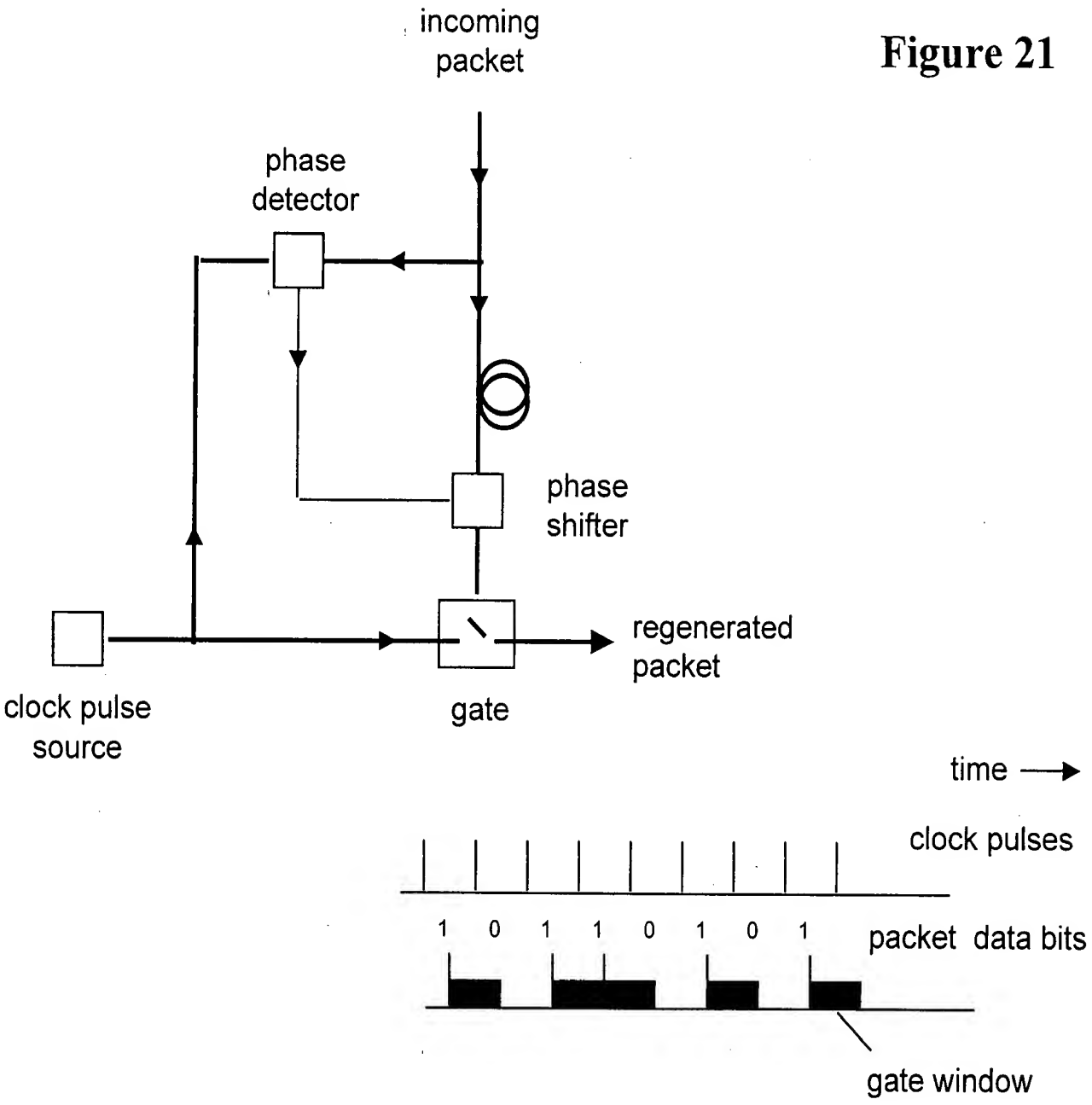
Figure 20



002260 "E879h960

Figure 21



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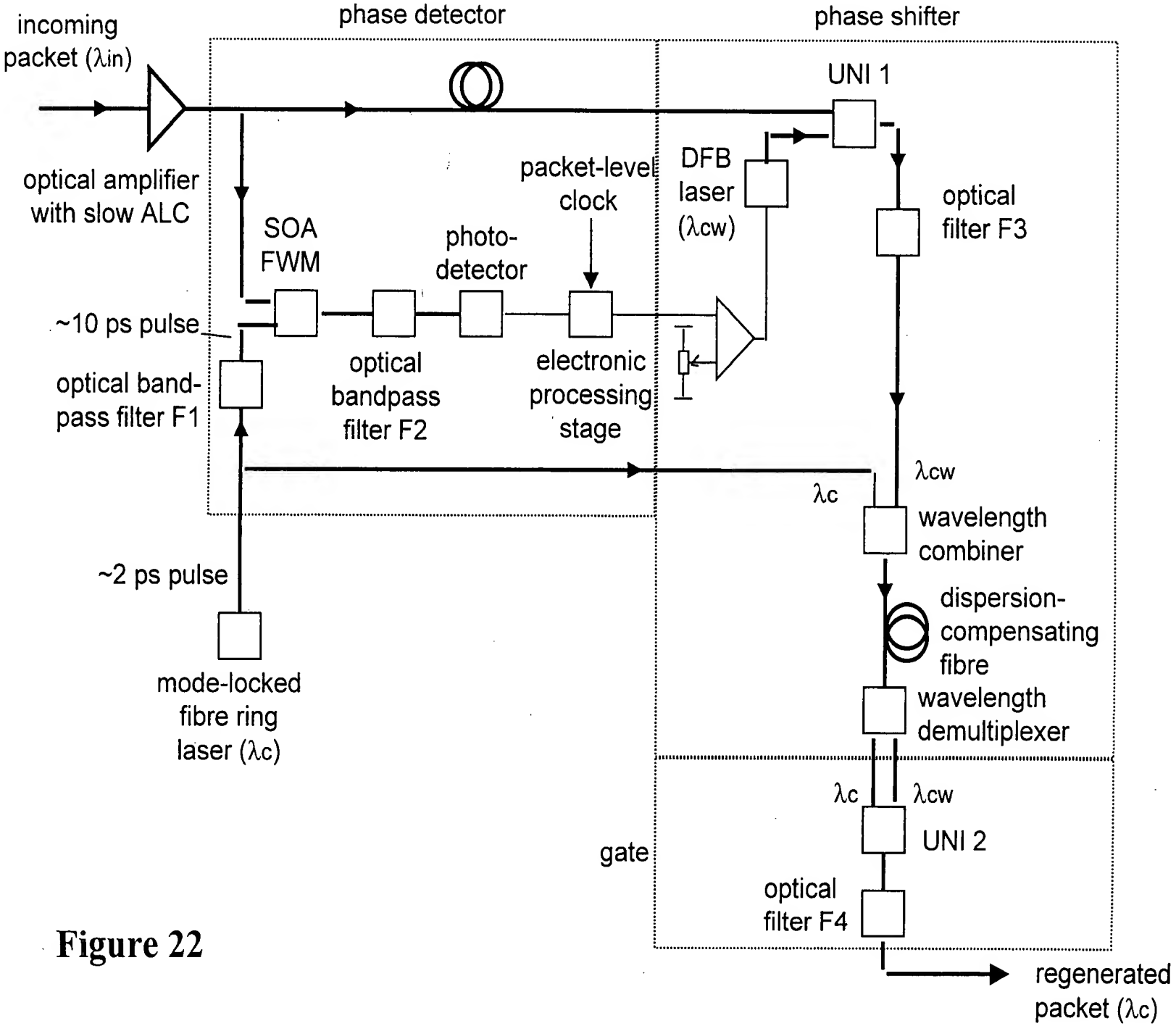

Figure 22

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002260 "E879h960

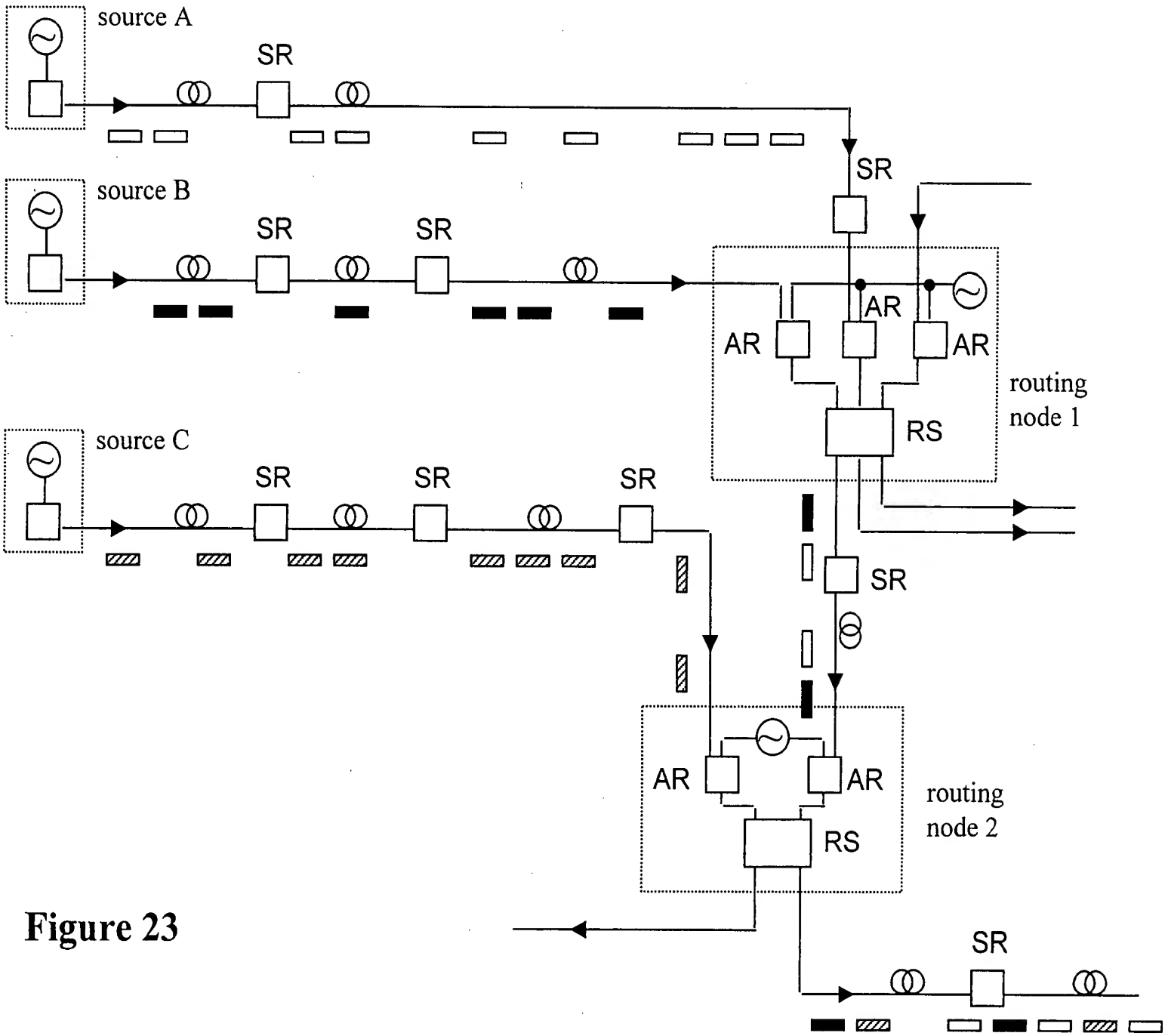

Figure 23

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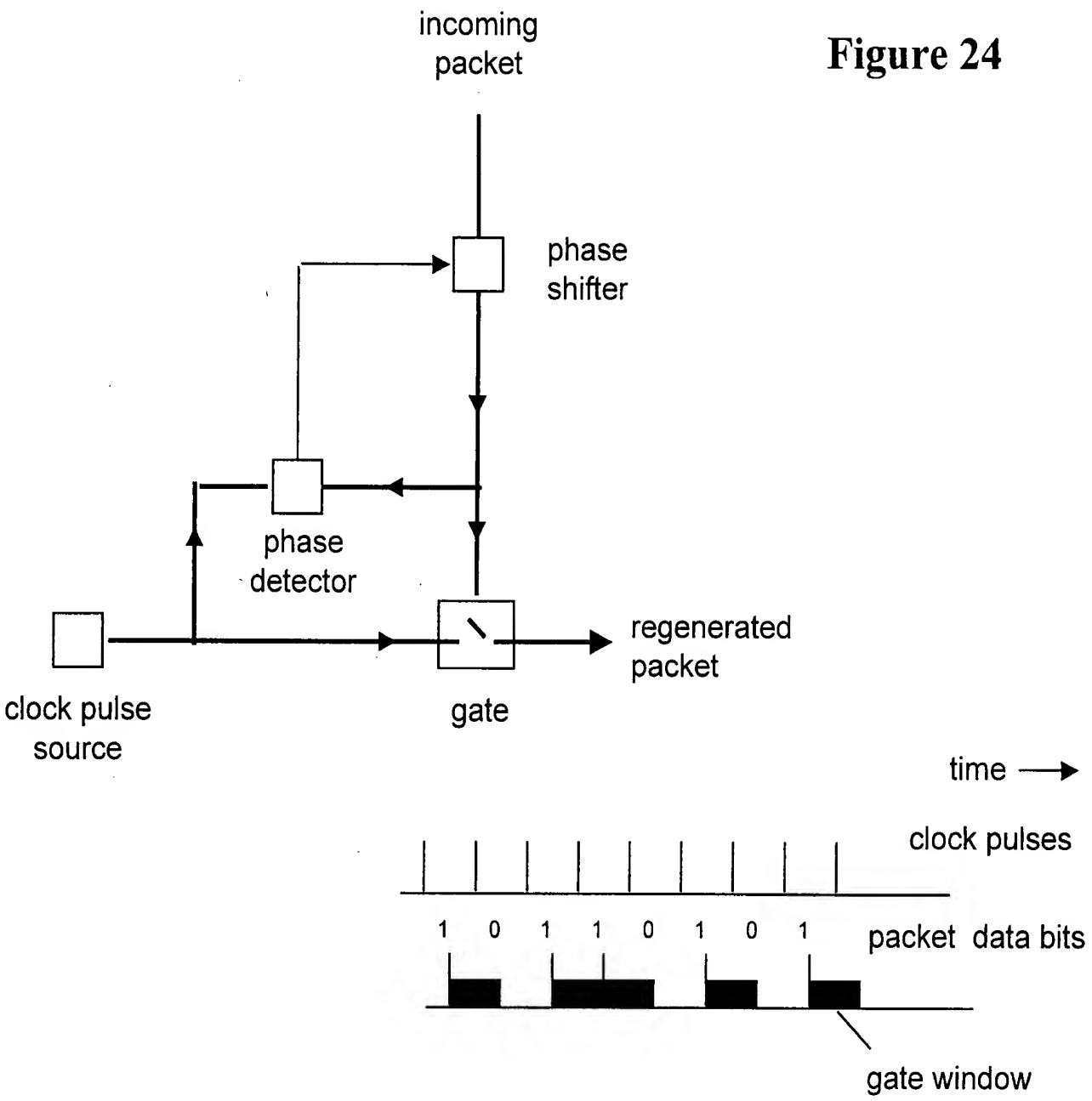
Figure 24



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002260" EB794960

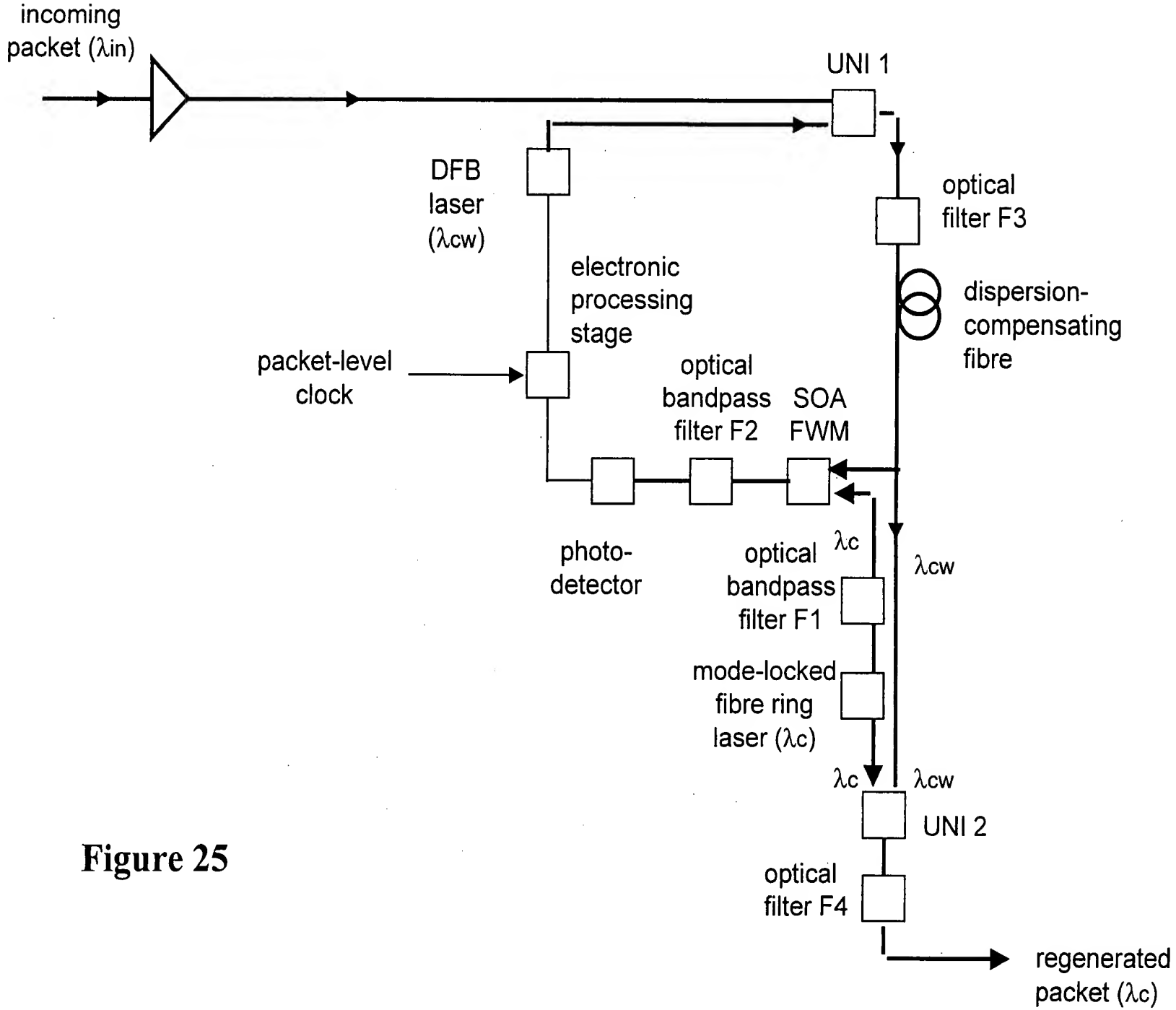

Figure 25

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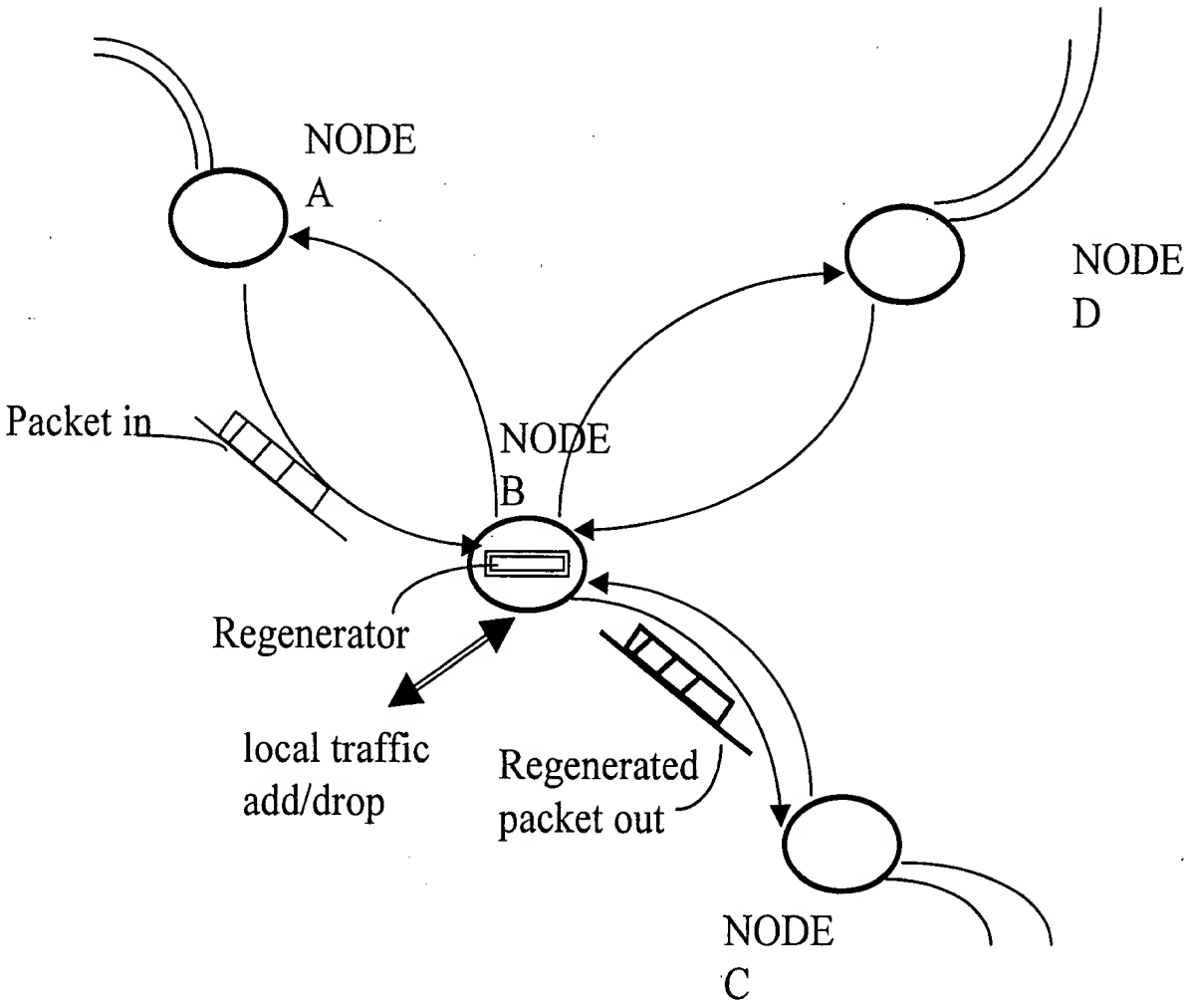

Figure 26

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